

AE-1238

B.Sc. (Part - II)
Term End Examination, 2016-17

ELECTRONICS

Paper - I

Digital Electronics

Time : Three Hours] [*Maximum Marks* : 50

Note : Answer **all** questions. All questions carry equal marks. Draw the suitable circuit diagram wherever necessary.

Unit-I

1. Write the method of binary to decimal conversion with proper example.

OR

Describe about OR gate with proper circuit diagram with truth table.

(2)

Unit-II

2. Explain DeMorgan's Theorem in detail.

OR

Describe about full-adder.

Unit-III

3. What do you mean by Transistor-Transistor Logic (TTL) ? Describe about two input TTL NAND gate.

OR

Describe about D flip-flop.

Unit-IV

4. With neat diagrams, explain the working of serial-in and serial-out shift register.

OR

Explain about the Decade Counter in detail.

Unit-V

5. Describe about ROM timing and types of ROM.

OR

(3)

Describe about timing and control of microprocessor with proper diagram.
